

Evan S. Kovanis



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PROFESSIONAL SUMMARY

Evan Kovanis is a research engineer at Zeidman Consulting. He has developed embedded firmware, GUI software, RF hardware, database application software, and ASIC verification code. Evan is certified in the use of CodeSuite®. He holds a bachelor's degree in Computer Engineering from the University of California Santa Barbara.

EXPERIENCE

03/2011 – present: Zeidman Consulting

- Research Engineer
- Runs CodeSuite® and analyzes results for software IP litigation
- Writes expert reports for software IP litigation
- Performs research on patents and trade secrets
- Writes claim charts for patent infringement analysis
- Develops software analysis tools

03/2011 – 12/2011: Smiths Corporation

- Systems Engineer
- Authored and contributed to technical white papers on topics such as transient voltages in an RF environment, the effects of undesired intermodulation, and Ethernet cable development and design
- Technical liaison between customers and the sales and engineering teams in new and existing RF, AC, Data, and DC projects
- Provided RF systems engineering expertise and support to customers and the sales team during development and launch of RF products
- Developed MS Windows Application for report generation and management of SQL database
- Served as the company's principle representative to the Antenna Interface Standards Group committee and as an AISG director

05/2006 – 03/2011: Smiths Corporation

- RF/Digital Design Engineer
- Principle developer of a unique firmware solution, hardware, and GUI test env. For an AISG protocol based communication system
- Developed code in C, C++, as well as C#. Involved in all aspects of product design, from board layout to software verification
- Lead engineer in a C# multi-threaded control system; designing the GUI and control logic for a new surge test facility
- Managed project flow and performed design of RF surge suppression devices from concept through prototype and into release

01/2005 – 09/2005: Fulcrum Microsystems

- ASIC Verification Engineer (Contract Position)
- Designed and contributed to the Java based test bench for a 10 Gigabit per port, 24 port Ethernet chip called Focal Point

- Also designed and wrote modules to verify the SERDES, EPL, Management, JTAG, SCAN, and XAUI portions of this ASIC
- Wrote Java, Perl, and Verilog and also worked closely with PCI-Express in verifying the stand alone EPL

11/2004 – 12/2004: Enerpro Inc.

- Embedded Device Programmer / Test Technician (Contract Position)
- Developed C for a PIC micro-controller using internal DAC and ADC functionality to build a closed feedback control circuit
- Debugged and implemented an automated test system using LabView and discrete components including muxes and decoders

07/2003 – 10/2003: Ccom Solutions

- Software Engineer
- Updated business software using Javascript, VB Script, HTML, and Java Networking

EDUCATION

B.S. Computer Engineering (Honors), University of California, Santa Barbara, 2004

T.E.C. (Technology Entrepreneurship Certificate), University of California, Santa Barbara, 2004

AWARDS & MEMBERSHIP

- National Society, Collegiate Scholars. Member (2001 – 2004) Officer (2002 – 2003)
- Dean's List Scholar Award. (Winter, Spring 2003; Fall, Winter, Spring 2004)
- Center for Engineering and Entrepreneurship Management. Member (2002 – 2004)
- Student Entrepreneur's Association. Member (2002 – 2004)

SPECIAL KNOWLEDGE AND SKILLS

- Embedded systems
- FPGA and CPLD design
- Java Database Connectivity (JDBC)
- Java Swing
- LAN Intercom
- Microprocessor design
- Operating Systems:
 - Linux
 - UNIX
 - Windows
- PCB layout
- Programming Languages:
 - C
 - C++
 - C#
 - SQL
 - Java
 - JavaScript
 - Perl
 - HTML
 - Verilog
 - VHDL
- RISC processor architecture
- Remote Method Invocation (RMI)
- Verilog
- XML