

Bob Zeidman



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PROFESSIONAL SUMMARY

Bob Zeidman has management experience in the founding and daily operation of various high-tech companies as well as hands-on experience designing, analyzing, and reverse-engineering hardware and software. Mr. Zeidman is considered a pioneer in the fields of analyzing and synthesizing software source code. Mr. Zeidman is also considered one of the leading experts in the Verilog hardware description language as well as ASIC and FPGA design. He has written several engineering texts and regularly teaches courses in these areas at conferences throughout the world. Mr. Zeidman is also an experienced and well-regarded expert in intellectual property disputes. Bob is certified in the use of CodeSuite®. He holds a B.A. in Physics and a B.S. in Electrical Engineering from Cornell University and a master's degree in Electrical Engineering from Stanford University.

EXPERIENCE

10/1987 - present: Zeidman Consulting

- Company founder and president.
- Provide engineering hardware and software design services to companies.
- Provide engineering support and expert witnesses for high-tech litigation.

8/2007 - present: Software Analysis and Forensic Engineering Corporation

- Company founder and president.
- Software tools for intellectual property litigation.
- Created patented CodeSuite® software including BitMatch®, CodeCLOC™, CodeCross®, CodeDiff®, CodeMatch®, and SourceDetective® for efficiently finding correlation between source code files of different programs.
- Architected CodeGrid for running CodeSuite® on a supercomputer grid.

1/2002 - present: Zeidman Technologies

- Company founder and president.
- Software tools for embedded software development.
- Created patented SynthOS™ software that automatically synthesizes source code for a real time operating system.
- Created patented Molasses® virtualization software that enables a slow speed hardware emulator or prototype to be attached to a high-speed network to emulate network hardware in a live system.

1/2003 – 4/2008: Advisor to Semizone.com

- Web-based training for engineers.
- Advisor on issues relating to e-learning content, development, presentation, and delivery.
- Instructor for various electrical engineering courses.

1/1999 - 12/2002: The Chalkboard Network

- Company founder and President.
- Web-based training for engineers and business professionals.

- Developed Depth Control[®], a unique instructional design methodology for Web-based training.

1/1997 - 12/1997: Consultant to Apple Computer

7/1995 - 9/1995

- Firmware development for a multimedia projection system.
- Firmware development for the Apple Studio Display, an advanced flat panel monitor.

7/1996 - 12/1996: Consultant to Hitachi Computer Products (America)

- Helped define an architecture and functional specification for an ATM switch.

10/1995 - 6/1996: Consultant to Cisco Systems

3/1995 - 10/1995

10/1994 - 1/1995

- Wrote Verilog behavioral models and developed a Verilog simulation environment for a 10baseT switch.
- Performed schematic capture, Verilog design, and simulation of an FDDI hub.
- Performed schematic capture, Verilog design, and simulation of an ATM router.

1/1992 - 4/1999: eVAULT Remote Backup Service

- Founded and ran the company.
- Invented the concept of remote backup.
- Set up a remote backup system with a central file server and communication lines including ISDN.
- Wrote remote backup software for DOS, Windows, and OS/2 including a GUI, a backup scheduler and file compression, encryption, and communication routines.

5/1997 - 6/1997: Consultant to Quickturn Design Systems

3/1995 - 7/1995

1/1994 - 2/1994

5/1993 - 6/1993

- Designed custom memory boards for emulation of a supercomputer.

7/1993 - 3/1994: Consultant to Adaptive Video

- Managed a team designing DSP-based medical imaging boards.

1/1993 - 3/1993: Consultant to Wireless Access

- Supervised the design of an FPGA-based prototype for a telecommunications encoding and decoding scheme.

6/1991 - 9/1991: Consultant to STEP Engineering

- Reviewed and optimized RISC hardware and software.

12/1990 - 8/1991: Consultant to RICOH Corporation

- Led a team that designed four RISC-based controllers for laser printers, scanners, faxes, and copiers.

9/1989 - 11/1990: Consultant to DAVID Systems

- Designed network interface boards for 10BaseT, 10Base2, and FOIRL.
- Led an international team that tested an AMI encoding scheme and designed an ASIC for a digital phone set.

5/1993 - 8/1993: Consultant to IKOS Systems

8/1992 - 2/1993

3/1988 - 8/1989

- Designed a high speed SBUS to MXIbus interface and a high-speed controller for a parallel processor.
- Architected, designed, and wrote diagnostic software for a RISC-based controller for a simulation accelerator.

10/1987 - 10/1988: Consultant to Stanford University

- Led a team of graduate students under Professor Mike Flynn in the design and testing of a neural network memory.

1/1986 - 3/1988: Telestream Corporation

- Developed an architecture model for a telecom parallel processor system.
- Modeled, simulated, and tested a proprietary bus.
- Designed a telecom processing element ASIC.
- Designed a telecom system backplane.

9/1985 - 12/1985: American Supercomputers

- Designed a data cache and the register section logic for a CRAY compatible supercomputer.
- Assisted with the implementation of a behavioral simulator.

1/1985 - 8/1985: ROLM Corporation

- Simulated, debugged, and tested cache and memory control ASICs for a minicomputer.
- Microcoded the character string instructions.

4/1983 - 12/1984: Signetics Corporation

- Project leader for a CMOS DMA Interface chip (68431).
- Simulated a TTL VME bus controller chip (68172).
- Redesigned and simulated an oxide isolated ISL Enhanced Video Attribute Controller chip (2675T).
- Simulated a TTL Disk Phase Locked Loop chip (68459).
- Created a standard procedure for developing ASICs.

LEGAL CONSULTING, EXPERT WITNESS

4/2011 – present: Motorola Mobility v. Microsoft

Law Firm: Sidley Austin

Client: Microsoft

Court: United States District Court, Southern District of Florida

Case: 1:10-cv-2406J-MORENO

- Alleged patent infringement of voicemail software.
- Alleged patent infringement of graphics software.
- Analyzed patents.
- Examined source code.
- Wrote expert reports.
- Testified in deposition.

8/2010 – 3/2011: Cross Match Technologies v. Suprema and Mentalix

Law Firm: Latham & Watkins

Client: Cross Match Technologies

Court: United States International Trade Commission

Investigation: 337-TA-720

- Alleged patent infringement of fingerprint scanning software.
- Analyzed patents.
- Examined source code.
- Compared software source code using CodeMatch.
- Wrote an expert report.
- Testified in deposition.

7/2010 – 4/2011: Xpoint Technologies v. Symantec

Law Firm: Quinn Emanuel Urquhart & Sullivan

Client: Symantec

Court: United States District Court, District of Delaware

Investigation: 09-CV-0026 (SLR)

- Alleged patent infringement of backup software.
- Analyzed patents.
- Examined software.
- Wrote an expert report.

4/2010 – present: Brocade v. A10 Networks

Law Firms: Orrick, Herrington & Sutcliffe; McDermott, Will & Emery

Client: Brocade

Court: U.S. District Court, Northern District Of California, San Jose Division

Case: C 10-03428 LHK (PSG)

- Alleged copying of software for network controllers.
- Compared software source code using CodeCross, CodeDiff, and CodeMatch.
- Measured software development using CodeCLOC.
- Wrote an expert report and several declarations.
- Testified in deposition.

3/2010 – 9/2010: Datamaxx v. Computer Products of Illinois

Law Firm: Ausley & McMullen

Client: Computer Products of Illinois

Court: U.S. District Court, Northern District Of Florida, Tallahassee Division

Case: 4:09cv435-RH/WCS

- Alleged copying of software to enable law enforcement.
- Compared software source code using CodeMatch.
- Wrote a declaration and a rebuttal expert report.
- Testified in deposition.

2/2010 – 7/2010: SplitFish v. Bannco

Law Firm: Finnegan, Henderson, Farabow, Garrett & Dunner, LLP

Client: SplitFish

Court: U.S. District Court, Northern Eastern District of Virginia, Alexandria Division

Case: I:10cv297

- Preliminary injunction against defendant granted.
- Alleged copying of firmware for video game controllers.
- Compared software source code using BitMatch.
- Wrote two declarations.

10/2009-11/2009: Sigma Six Technologies and Sigma Six Consulting. v. Nagarro and T-Systems Enterprise Services.

Law firm: Ropers, Majeski, Kohn & Bentley PC

Client: Nagarro

Court: U.S. District Court, Northern District of California

Case: C 08-05633 JW

- Case was settled.
- Alleged trade secret infringement case involving enterprise client user interface software.

8/2009 – 11/2010: Zynga v. Green Patch

Law firm: Quinn Emanuel Urquhart & Sullivan

Client: Zynga

Court: United States District Court, Northern District of California

Case: CV-09-3636 SC (EMC)

- Case was settled.
- Alleged copyright infringement case involving web-based social networking game software.
- Compared software source code using CodeMatch.
- Wrote two declarations and an expert report.

8/2009 – present: De Lage Landen Operational Services v. Third Pillar Systems

Law Firm: Wilson Sonsini Goodrich & Rosati

Client: Third Pillar Systems

Court: U.S. District Court, Eastern District of Pennsylvania

Case: 09-cv-02439-HB

- Case was adjudicated.
- Alleged trade secret misappropriation and contract dispute involving lending and leasing software.
- Compared software source code using CodeMatch and CodeDiff.
- Wrote an expert report and a rebuttal expert report.
- Testified in deposition.
- Testified at trial.

2/2009 – 10/2009: Minden Schipper & Associates v. Cancercare Manitoba (Varian Medical Systems)

Client: Varian Medical Systems

Court: Queen's Bench, Winnipeg Centre, Canada

Case: CI 05-01-45377

- Case was settled.
- Alleged trade secret misappropriation and copying of oncology diagnosis expert system software.
- Compared software source code using CodeMatch.
- Wrote an expert report.

11/2008 – 1/2010: Applied Materials v. Advanced Micro-Fabrication Equipment Co.

Law firm: Goodwin Procter

Client: Applied Materials

Court: U.S. District Court, Northern District Of California

Case: C07 05248 JW (PVT)

- Case was settled.
- Alleged copying of software for semiconductor manufacturing machines.
- Compared software source code using CodeMatch.
- Wrote expert reports.
- Testified in deposition.

9/2008 – 9/2009: Abanco Investments v. GuestLogix

Law firm: Patterson, Thuente, Skaar & Christensen

Client: GuestLogix

Court: U.S. District Court, Northern District of Illinois

Case: 07 C 1071

- Case was settled.
- Alleged trade secret theft involving point-of-sale software.
- Compared software source code using CodeMatch.

- Wrote an expert report.

9/2008 – 3/2010: Personnel Department v. CareerBuilder

Law firm: Jones, Day, Reavis & Pogue
 Client: CareerBuilder
 Court: U.S. District Court, District of Vermont
 Case: 2:08-cv-59-wks

- Case was settled.
- Alleged trade secret theft involving resume building software.
- Compared software source code using CodeMatch.
- Wrote an expert report.

9/2008 - 12/2008: Omniplanar v. Datalogic and Datalogic Scanning

Law firm: Robins, Kaplan, Miller & Ciresi
 Client: Omniplanar
 Court: United States District Court, District of New Jersey
 Case: 1:2008cv05234

- Case was settled.
- Alleged copyright infringement and trade secret theft involving bar code scanner firmware.
- Compared software source code to software binary code using BitMatch.

7/2008-10/2008: Facebook v. StudiVZ

Law firm: Orrick, Herrington & Sutcliffe
 Client: Facebook
 Court: United States District Court, Northern District of California
 Case: 5:08-CV-03468 JF

- Case was settled.
- Alleged copyright infringement case involving web-based social networking software.
- Compared software source code using CodeMatch.

4/2008 – 12/2010: Esbin & Alter v. Zagppier, et al.

Law firm: Alter & Alter
 Client: Esbin & Alter
 Court: U.S. District Court, Southern District of New York
 Case: 08 Civ. 313

- Contract dispute case involving billing and document management software.
- Compared software source code using CodeMatch.
- Wrote two declaration and two expert reports.

4/2008 – 12/2008: Piper Jaffray v. Vermilion Capital Management

Law firm: Patterson, Thuente, Skaar & Christensen
 Client: Vermilion Capital Management
 Court: Minnesota District Court, Fourth Judicial District, County of Hennepin
 Case: 07-20203

- Alleged trade secret case involving stock market technical analysis software.
- Compared software source code using CodeCross, CodeDiff, and CodeMatch.
- Wrote an expert report.

4/2008 – 11/2008: Intelligraphics v. Marvell Semiconductor

Law firm: Sommers and Schwartz
 Client: Intelligraphics
 Court: U.S. District Court, Northern District Of California – San Francisco Division
 Case: C-07-2499 JCS

- Contract dispute case involving WLAN firmware and drivers.

- Compared software source code using CodeDiff.
- Wrote an expert report.
- Testified at deposition.

3/2008 – 8/2008: Optovue v. Carl Zeiss Meditec

Law firm: Nixon Peabody

Client: Carl Zeiss Meditec

Court: U.S. District Court, Northern District of California - Oakland Division

Case: C 07-03010 CW

- Case was settled.
- Alleged copyright/trade secret theft case involving optical coherence tomography software.
- Compared software source code using CodeMatch.
- Wrote an expert report.

2/2008 – 4/2008: Gemstar v. Digeo

Law firm: Ropes & Gray

Client: Gemstar

Court: U.S. District Court, Central District of California – Western Division

Case: CV-06-6519

- Case was settled.
- Alleged patent infringement case involving program guide displays.
- Wrote an expert report.

1/2008 – present: MSC Software v. Altair Engineering, et al.

Law firm: Dykema Gossett

Client: MSC Software

Court: U.S. District Court, Eastern District of Michigan – Southern Division

Case: 2:07-cv-12807

- Alleged trade secret theft case involving motion simulation software.
- Compared software source code using CodeMatch.
- Wrote a declaration and an expert report.
- Testified at a hearing.
- Testified in deposition.

5/2007 – 12/2008: The MathWorks v. COMSOL

Law firm: Jones, Day, Reavis & Pogue

Client: The MathWorks, Inc.

Court: U.S. District Court, Eastern District of Texas – Tyler Division

Case: 6:06-CV-335

- Case was settled.
- Alleged copyright infringement case involving mathematical modeling software.
- Compared software source code using CodeMatch.

5/2007 – 12/2008: The MathWorks v. COMSOL

Law firm: Jones, Day, Reavis & Pogue

Client: The MathWorks, Inc.

Court: U.S. District Court, Eastern District of Texas – Tyler Division

Case: 6:06-CV-334

- Case was settled.
- Alleged patent infringement case involving mathematical modeling software.

5/2007 – 6/2007: Third Party Verification v. SignatureLink

Law firm: Law Offices of Brian S. Steinberger

Client: Third Party Verification

Court: U.S. District Court, Middle District of Florida

Case: 6:06-cv-00415

- Case was settled.
- Alleged copyright infringement case involving web-based signature capture software.
- Compared software source code using CodeMatch.
- Wrote an expert report.

4/2007 – 7/2008: Symantec v. Commissioner of Internal Revenue

Law firm: Baker & McKenzie

Client: Symantec

Court: US Tax Court

Case: 12075-06

- Case was adjudicated.
- Software tax dispute
- Software comparison using CodeDiff.
- Wrote an expert report.
- Testified at trial.

4/2007 – 5/2007: Kernius & Frise v. International Electronics

Law firm: Zito tlp

Client: Kernius & Frise

Case: 05-CV-1927

Court: U.S. District Court, District of Maryland

- Alleged patent infringement case involving modems and call waiting.
- Wrote an expert report.
- Testified in deposition.

2/2007 – 8/2008: Quantum Research Group (Atmel) v. Apple, Cypress, Fingerworks

Law firm: Zito tlp, Sidley Austin

Client: Quantum Research Group (Atmel)

Court: U.S. District Court, District of Maryland

Case: 05-cv-03408-WMN

- Alleged patent infringement case involving capacitive sensing devices.
- Assisted with claim construction.
- Wrote an expert report.
- Testified in deposition.

8/2006 – 8/2007: AdTech RFID v. Adept Identification Technologies, et al.

Law firm: Wilson Sonsini Goodrich & Rosati

Client: Adept Identification Technologies

Court: Superior Court of Santa Clara County California

Case: 1:06-CV-057464

- Case was settled.
- Alleged trade secret theft case involving RFID software.
- Compared software source code using CodeMatch.
- Wrote an expert report.

6/2006 - 2/2007: Medinformatix v. AcerMed

Law firm: Timothy McGonigle

Client: MedInformatix

Arbitration: JAMS

Ref: 1220035252

- Case was decided at arbitration.
- Alleged trade secret case involving electronic medical records software.

- Assisted with determination of trade secrets.
- Compared software source code using CodeMatch.
- Wrote declarations.
- Testified in deposition.

5/2006 - 11/2006: Iconix v. netPickle, et al.

Law firm: Orrick, Herrington & Sutcliffe

Client: netPickle

Court: U.S. District Court, Northern District of California – Oakland Division

Case: 4:06-cv-02201

- Case was settled.
- Alleged copyright infringement case involving web-based presentation software.
- Compared software source code using CodeDiff.
- Wrote a declaration.
- Wrote an expert report.
- Testified in deposition.

4/2006 - 10/2006: Forgent v. Microsoft et al.

Law firm: Susman Godfrey

Client: Forgent

Court: U.S. District Court, Northern District of California – San Jose Division

Case: M:05-CV-01654

- Case was settled.
- Alleged patent infringement case involving JPEG encoding of pictures in files.
- Reverse engineered video equipment.

2/2006 – 5/2008: Rasterex Holdings v. Research in Motion

Law firm: Kilpatrick Stockton

Client: Rasterex Holdings

Court: Superior Court of Fulton County, State of Georgia

Case: 2003-cv-76785

- Case was settled.
- Alleged copyright infringement case involving mobile document translation and storage software.
- Compared software source code using CodeMatch.
- Wrote an expert report.
- Testified in deposition.

2/2006 - 9/2006: Medinformatix v. Camtronics Medical Systems

Law firm: Timothy McGonigle

Client: MedInformatix

Court: U.S. District Court, Central District of California

Case: 2:05-cv-04829 SJO

- Case was settled.
- Alleged trade secret theft case involving electronic medical records software.
- Assisted with determination of trade secrets.
- Wrote declarations.

8/2005 – 12/2007: Moneygram Payment Systems v. Enterprise Payment Solutions

Law firm: Michael Best & Friedrich LLP

Client: Moneygram

Court: U.S. District Court, Eastern District of Tennessee

Case: 1:05-cv-00172

- Alleged copyright infringement case involving ACH financial software.
- Compared software source code using CodeMatch.

- Wrote an expert report.

8/2005 - 8/2006: Silvaco v. Specular

Law firm: Wilson Sonsini Goodrich & Rosati

Client: Specular

Court: Superior Court of Santa Clara County California

Case: 1:04-cv-031951

- Case was settled.
- Alleged trade secret case involving EDA software.
- Compared software source code using CodeMatch.
- Wrote an expert report.

7/2005-2/2008: ConnectU v. Facebook, et al.

Law firm: Orrick, Herrington & Sutcliffe

Client: Facebook

Court: United States District Court, District of Massachusetts

Case: 1:04-cv-11923

- Case was settled.
- Alleged copyright infringement case involving social network software.
- Compared software source code using CodeMatch.

4/2005 – 3/2007: Merchant Transaction Systems v. Nelcela

Law firm: Lewis & Roca

Client: Merchant Transaction Systems/POST Integration/Ebocom

Court: U.S. District Court, District of Arizona

Case: 2:02-cv-01954

- Alleged copyright infringement involving credit card processing software.
- Compared software source code using CodeMatch.
- Wrote an expert report.
- Testified in deposition.

4/2005: Brod v. Lev, et al.

Law firm: Wilson Sonsini Goodrich & Rosati

Client: Lev

Court: Superior Court of Santa Clara County California

Case: 1:03-cv-005813

- Case was dismissed.
- Alleged copyright infringement case involving Internet acceleration software.
- Compared software source code using CodeMatch.

3/2005 - 12/2005: American Video Graphics v. Electronic Arts, et al.

Law firm: McKool Smith

Client: AVG

Court: U.S. District Court, Eastern District of Texas – Tyler Division

Case: 6:04-CV-398

- Case was settled.
- Alleged patent infringement case involving 3D graphics software algorithms.
- Examined source code for over 50 video games.
- Wrote claim charts for each video game.

3/2005 - 12/2005: American Video Graphics v. Sony Corporation of America, et al.

Law firm: McKool Smith

Client: AVG

Court: U.S. District Court, Eastern District of Texas – Tyler Division

Case: 6:04cv399

- Case was settled.
- Alleged patent infringement case involving 3D graphics hardware algorithms.
- Examined graphic chips.

8/2004 – 12/2006: Creative Science Systems v. Forex Capital Markets

Law firm: Baker & McKenzie/Sommers and Schwartz

Client: Creative Science Systems

Court: U.S. District Court, Northern District of California

Case: 5:04-cv-03746

- Case was settled.
- Alleged copyright infringement involving web-based financial software.
- Compared software source code using CodeMatch.
- Compared software object code.
- Wrote a declaration and an expert report
- Testified in deposition.

8/2004 - 3/2005: XIOTech v. Compellent Technologies, et al.

Law firm: Faegre & Benson

Client: Compellent

Court: Minnesota District Court, Fourth Judicial District, County of Hennepin

Case: 04-5065

- Case was settled.
- Alleged trade secret theft involving storage area network (SAN) software.
- Compared software source code using CodeMatch.
- Compared features and researched prior art for storage area network (SAN) software.
- Wrote an expert report.

8/2004 – 9/2004: OpenTable v. Smart Restaurant Solutions

Law firm: Wilson Sonsini Goodrich & Rosati

Client: Smart Restaurant Solutions

Court: Superior Court of the State of California for the County of San Francisco

Case: CGC-03-424516

- Judgment at trial.
- Alleged copyright infringement involving restaurant management software.
- Compared software source code using CodeMatch.
- Wrote an expert report

7/2004 - 2/2005: Zoran and Oak Technology v. MediaTek, et al.

Law firm: Wilson Sonsini Goodrich & Rosati /Hogan & Hartson

Client: MediaTek

Court: U.S. International Trade Commission

Investigation: 337-TA-506

- Case was settled.
- Alleged patent infringement involving CD-ROM/DVD controller hardware.
- Examined VHDL for two CD-ROM/DVD controller chips to determine their architectures and implementations.
- Wrote an expert report and created exhibits for trial.
- Testified in deposition.
- Testified at trial.

4/2004 - 11/2004: Agere Systems v. Intersil

Law firm: Kirkland & Ellis

Client: Agere

Court: U.S. District Court, Eastern District of Pennsylvania

Case: 02-CV-08219, 02-CV-1544

- Case was settled.
- Alleged copyright infringement and contract dispute involving WLAN chips.
- Compared firmware source code using CodeMatch.
- Examined Verilog and VHDL source code.
- Wrote an expert report and rebuttal to opposition expert report.

4/2003 - 5/2003: Alvis v. Hewlett-Packard

Law firm: Drinker Biddle & Reath

Client: Hewlett Packard

Court: U.S. District Court, District Court of Jefferson County, Texas

Case: A-164,880

- Case was settled.
- Class action suit involving reliability of floppy disk drives and software patches.
- Wrote an expert report.
- Testified in deposition.

4/2003 - 5/2003: MediaTek Software Clean Room Development Project

Law firm: MacPherson Kwok

Client: MediaTek

- Clean room code development.
- Reviewed source code and compared different source code routines for similarities.

3/2003 - 5/2003: Research In Motion v. Good Technology

Law firm: Jones, Day, Reavis & Pogue

Client: Research In Motion

Court: U.S. District Court, District of Delaware

Case: 02-556-JJF, 02-1286-JJF, 02-1338-JJF

- Case was settled.
- Alleged patent infringement case involving handheld wireless devices and supporting software.
- Analyzed software source code.
- Assisted with deposition of opposing expert.
- Wrote claim charts.

8/2001 - 9/2001: Intel v. VIA Technologies

Law firm: Howrey Simon Arnold & White

Client: Intel

Court: U.S. District Court, Northern District of California

Case: C99-03062

- Case was settled.
- Alleged patent infringement case involving computer motherboards.
- Examined computer motherboards for patent infringement.

7/2001 - 4/2003: Intel v. VIA Technologies

Law firm: Dewey Ballantine/Brobeck, Phleger & Harrison

Client: Intel

Court: U.S. District Court, Western District of Texas

Case: A-01-CA-602-SS

- Case was settled.
- Alleged patent infringement cases involving CPUs and computer chipsets.
- Assisted with claim construction.
- Wrote test case software in assembly language.
- Examined computer motherboards.

- Analyzed Verilog code of CPUs.
- Wrote several expert reports.
- Wrote several claim charts.

3/2001 - 3/2001: KRS Distributing v. Gatten Insurance

Law firm: Stone & Hiles

Client: Gatten Insurance

- Insurance claim.
- Examined a fax machine to retrieve stored documents.

7/1997 - 3/1999: Texas Instruments v. Hyundai Electronics Industries Co.

Law firm: Jones, Day, Reavis & Pogue

Client: Texas Instruments

Court: U.S. District Court, Eastern District of Texas

Case: 2:98CV74

- Case was settled.
- Alleged patent infringement involving semiconductor wafer handling hardware, software, and communication protocols.
- Reverse engineered hardware and software in order to determine infringement.
- Constructed exhibits.
- Assisted with the writing of expert reports.
- Assisted with the writing of claim charts.

8/1996 - 12/1996: Texas Instruments v. Samsung Electronics, et al.,

Law firm: Jones, Day, Reavis & Pogue

Client: Texas Instruments

Court: U.S. District Court, Eastern District of Texas

Case: 2:96-CV-1, 2:96-CV-2

- Case was settled.
- Alleged patent infringement involving semiconductor wafer handling hardware, software, and communication protocols.
- Reverse engineered hardware and software in order to determine infringement.
- Constructed exhibits.
- Assisted with the writing of expert reports.
- Assisted with the writing of claim charts.

2/1996 - 7/1996: Cirrus Logic v. Agarwal, et al.

Law firm: Morrison & Foerster

Client: Cirrus Logic

Court: Superior Court of Santa Clara County California

Case: CV 745373

- Case was settled.
- Alleged trade secret case involving semiconductors and LCD display technology.
- Examined validity of trade secrets.
- Reconstructed the history of an internal engineering project.
- Researched prior art.

HONORS, AWARDS, AND DISTINCTIONS

Engineering and Science

- Final Round, 2011 Jolt Awards, for the book *The Software IP Detective's Handbook: Measurement, Comparison, and Infringement Detection*.
- Outstanding Engineer in a Specialized Field: For Innovative Contributions in the Area of Forensic Software Analysis, IEEE Santa Clara Valley Section, 2010.

- Session's Best Paper Award, The 11th World Multi-Conference on Systemics, Cybernetics and Informatics, 2007.
- The Number 5 Programmable Logic "How To" article of 2006, Programmable Logic DesignLine newsletter.
- Finalist, Design News magazine 2006 Golden MouseTrap Award: Design and Development Software Tools, for SynthOS.
- Winner, Software Development magazine 2003 Jolt Reader's Choice Award for the book *Designing with FPGAs and CPLDs*.
- Senior Member, IEEE
- Top PLD/FPGA News and Feature Article for 2003, CMP Media
- Winner, Wyle/EETimes American By Design Contest
- Stanford Graduate Engineering Fellowship
- Eta Kappa Nu (Electrical Engineering honor society)
- Association for Educational Data Systems Honorable Mention
- Bausch & Lomb Honorary Science Award

Writing and Filmmaking

- Semifinalist, November 2011 Amazon Studios Best Kids and Family Script Award, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Semifinalist, October 2011 Amazon Studios Best Script Award, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Semifinalist, September 2011 Amazon Studios Best Script Award, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Semifinalist, August 2011 Amazon Studios Best Script Award, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Semifinalist, July 2011 Amazon Studios Best Script Award, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Semifinalist, June 2011 Amazon Studios Best Sci-Fi/Action Script Award, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Semifinalist, June 2011 Amazon Studios Best Script Award, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Semifinalist, 2004 Cinequest Screenwriting Competition, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Second Place, 2002 Autumn Moon Productions Screenplay Awards, for the screenplay "Horror Flick."
- Third Place, 2002 Autumn Moon Productions Screenplay Awards, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- First Place, 2001 Focus on Writers Contest, for the screenplay "Horror Flick."
- Special Mention Winner, 2001 Screenwriting Showcase Awards, for the screenplay "Horror Flick."
- Honorary Mention, 2002 Autumn Moon Productions Screenplay Awards, for the screenplay "Sex and Violence."
- Finalist, 2001 Empyrion Screenplay Competition, for the screenplay "Sex and Violence."
- Finalist, 2001 New Century Writer Awards, for the screenplay "Horror Flick."
- Top Ten Finalist, 2001 Tennessee Screenwriting Association Competition, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Top Finalist, BDR 2000 Productions New Millennium Screenplay 2001 Contest, for the screenplay "Horror Flick."
- Certificate of Merit, 2002 International Screenplay Competition, for the screenplay "Horror Flick."
- Certificate of Merit, 2002 International Screenplay Competition, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Certificate of Merit, 2002 International Screenplay Competition, for the screenplay "Sex and Violence."
- Semifinalist, 2001 WordsFromHere Contest, for the screenplay "Horror Flick."

- Semifinalist, 2001 Venice Arts Screenwriting Competition, for the screenplay "Horror Flick."
- Semifinalist, 2001 National Screenwriting Competition, for the screenplay "Horror Flick."
- Semifinalist, 2001 National Screenwriting Competition, for the screenplay "Sex and Violence."
- Quarterfinalist, 2001 Fade In: Screenwriting Awards, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Quarterfinalist, Texas Film Institute 2001 Screenplay Competition, for the screenplay "Horror Flick."
- Certificate of Merit, Writer's Digest 2000 National Self-Published Book Awards, for the novel "Horror Flick."
- Semifinalist, 2000 poetry.com North American Open Poetry Contest, for the poem "I Remember."
- Quarterfinalist, 1999 New Century Writer Awards, for the novel "Horror Flick."
- Quarterfinalist, 1999 New Century Writer Awards, for the screenplay "The Amazing Adventure of Edward and Dr. Sprechtmachen."
- Third Place, 1998 Magnum Opus Discovery Awards of the C.C.S. Entertainment Group and the Hollywood Film Festival, for the novel "Horror Flick."
- Quarterfinalist, 1998 Empire Screenplay Contest, for the screenplay "Sex and Violence."
- A reading of my screenplay "Sex and Violence" was performed by the Independent Media Artists Group (IMAGE), on August 15th, 1998.
- Vermont Studio Center scholarship to attend a one-month writing retreat, April 1998.
- Semifinalist, 1997 Monterey County Film Contest, for the screenplay "Sex and Violence."
- First Place, 1993 Foster City Annual Writer's Contest, for unpublished short story "The Contest."
- Semifinalist, 1993 national Syndicated Fiction Project, for unpublished short story "The Contest."
- First Place, 1990 Foster City Annual Writer's Contest, for unpublished short story "The Lost and Found Virginity."
- 1989 Philadelphia International Film Festival showing of the short film "Writer's Block."
- First Place, 1988 Fremont Film Festival, for the short film "February 20, 1988."

Miscellaneous

- United Synagogue Award for Excellence
- Biography in Who's Who in America
- University Unions Distinguished Service Award
- Phi Beta Kappa
- Ivy League Honor Society
- Alpha Lambda Delta honor society
- Dual Degree Program, Cornell University
- National Merit Scholarship
- Literary Society Foundation Award Gold Medal, Excellence in German
- Literary Society Foundation Award Bronze Medal, Excellence in German
- Rumsey Scholarship, Cornell Club of Philadelphia
- City of Philadelphia Scholarship
- School District of Philadelphia Scholarship
- Fourth Prize, Colonial Philadelphia Historical Society Essay Contest
- Founder, Delaware Valley Teen Mensa

SPECIAL KNOWLEDGE AND SKILLS

- CodeSuite certified.
- Software source code analysis and synthesis
- Computer architectures: AMD 29000, CRAY XMP, Data General MV8000, IBM PC, IDT R4650, Intel 8051, Intel x86 family, Motorola 68000, 68HC11, 68HC08, 68HC05, TI TMS320Cxx, TMS340xx
- Networking protocols: ATM, Ethernet
- Buses: ADB, EISA I²C, ISA, MXI, PCI, SBUS, VME

- Hardware programming languages: ABEL, AHDL, CUPL, PALASM, Verilog, VHDL
- Software programming languages: APL, BASIC, C, C++, Delphi, FOCAL, FORTRAN, Java, LISP, Pascal, Perl, PHP, PL/1, PowerBuilder, SQL, Visual BASIC, various assembly, machine languages
- Operating systems: AOS/VS, MSDOS, UNIX, VMS, Windows 3.1/NT/9x/2000/XP/Vista
- Workstations: Apple Macintosh, Daisy, IBM PC, Hewlett Packard, SUN, VALID
- Electronic Design Automation (EDA)
- CAD tools: Concept, Futurenet, MAX+Plus II (Altera), MDE (LSI Logic), Mentor, Orcad, PROCapture, P-CAD, Schema, Viewlogic, XACT (Xilinx)
- Simulation accelerators: Mentor Graphics, Cadence Design Systems
- Hardware emulators: Mentor Graphics, Cadence Design Systems
- ASIC design
- FPGA and CPLD design: Actel, Altera, Lattice, Xilinx
- Miscellaneous design experience: Cache memory, telecommunications, data communications, digital signal processing (DSP), digital logic (CMOS, ECL, TTL), analog
- Patent infringement
- Trade secret theft
- Copyright infringement
- Plagiarism detection
- Solid state theory
- Information theory

EDUCATION

Master of Science in Electrical Engineering, 1982, Stanford University

Bachelor of Science with distinction in Electrical Engineering, 1981, Cornell University

Bachelor of Arts cum laude in Physics and with distinction in all subjects, 1981, Cornell University

BOOKS

1. Bob Zeidman, *The Software IP Detective's Handbook: Measurement, Comparison, and Infringement Detection*, Prentice-Hall, Upper Saddle River, NJ, 2011, 450pp.
2. Clive Maxfield, *FPGAs: World Class Designs*, Elsevier Inc., Burlington, MA, 2009, Chapter 1 (reprint).
3. Ashby, Baker, Ball, Crowe, Hayes-Gill, Hickman, Kester, Mancini, Grout, Pease, Tooley, Williams, Wilson, Zeidman, *Circuit Design: Know It All*, Elsevier Inc., Burlington, MA, 2008, Chapters 27-29 (reprint).
4. R. C. Cofer, Clive Maxfield, Bob Zeidman, Richard Munden, Rick Gentile, *Newnes FPGAs: ebook Collection*, Elsevier Science & Technology Books, Burlington, MA, 2008 (reprint).
5. Bob Zeidman, *Designing with FPGAs and CPLDs*, CMP Books, Lawrence, KS, 2002, 220pp.
6. Bob Zeidman, *Introduction to Verilog*, IEEE Press, Piscataway, NJ, 2000, 99pp.
7. Bob Zeidman, *Verilog Designer's Library*, Prentice-Hall, Upper Saddle River, NJ, 1999, 411pp.
8. Bob Zeidman, *Horror Flick*, Zeidman Consulting, Cupertino, CA, 1999, 210pp.
9. Bob Zeidman, *The Amazing Adventure of Edward and Dr. Sprechtmachen*, Zeidman Consulting, Cupertino, CA, 1998, 52pp.

PAPERS AND PRESENTATIONS

1. Zeidman, Bob, "Setting the Record Straight: Patent Trolls vs. Progress" *IPWatchdog* (www.ipwatchdog.com), May 1, 2012.

2. Baer, N. and Zeidman, B., "Measuring Whitespace Pattern Sequences as an Indication of Plagiarism," *Journal of Software Engineering and Applications*, 2012, April 2012.
3. Zeidman, Bob, "Will Congress Break the Internet?" *IPWatchdog* (www.ipwatchdog.com), February 2, 2012.
4. Zeidman, Bob, "The Case of the Arrogant Expert," *Intellectual Property Today*, February 2012.
5. Zeidman, Bob, "The Software IP Detective: Infringement Detection in a Nutshell," *IPWatchdog* (www.ipwatchdog.com), November 20, 2011.
6. Zeidman, Bob, "How Do I Infringe Thee? Let Me Count the Ways," *InformIT* (<http://www.informit.com>), Sep 21, 2011.
7. Zeidman, Bob, "The history of digital game intellectual property from Atari to Zynga," The Museum of Art and Digital Entertainment, August 4, 2011.
8. Shay, I., Baer, N., and Zeidman, R., "Measuring Whitespace Patterns in Computer Source Code as an Indication of Plagiarism," *Intellectual Property Today*, October 2010.
9. Zeidman, B., "Software v. Software," *IEEE Spectrum*, October 2010.
10. Shay, I., Baer, N., and Zeidman, R., "Measuring Whitespace Patterns as an Indication of Plagiarism," ADFSL Conference on Digital Forensics, Security and Law, May 20, 2010.
11. Hoehn, T. and Zeidman, R., "Measuring the Speedup of a Commercial Application on a Computer Grid," ISCA 22nd International Conference On Parallel And Distributed Computing And Communication Systems, September 24, 2009.
12. Zeidman, R., "DUPE: The Depository of Universal Plagiarism Examples," 5th International Conference on IT Security Incident Management & IT Forensics, September 2009.
13. Baer, N. and Zeidman, B., "Measuring Changes in Software with CLOC," *Embedded.com* (www.embedded.com), July 28, 2009.
14. Baer, N. and Zeidman, B., "Measuring Changes in Software IP," *Intellectual Property Today*, June 2009.
15. Baer, N. and Zeidman, B., "Measuring Software Evolution with Changing Lines of Code," 24th International Conference on Computers and Their Applications (CATA-2009), April 10, 2009.
16. Zeidman, B., "Detecting and Proving Software Theft and Infringement," SDForum: Emerging Tech SIG, December 10, 2008.
17. Zeidman, B., "Find Your Copy-Cats With SCC," *Software Test & Performance*, October 2008, pp 18-23.
18. Zeidman, B., "Software Intellectual Property," invited guest lecture, NALSAR University of Law, Hyderabad, India, October 27, 2008.
19. Zeidman, B., "Detecting Stolen Code," EDAC Anti-Piracy Committee, June 12, 2008.
20. Zeidman, R., "Multidimensional Correlation of Software Source Code," The Third International Workshop on Systematic Approaches to Digital Forensic Engineering, May 22, 2008.
21. Zeidman, B., "Getting Better Search Results," *Dr. Dobb's Journal*, May 2008, pp 44-48.
22. Zeidman, B. and Baer, N., "What, Exactly, Is Software Trade Secret Theft?" *Intellectual Property Today*, March 2008.
23. Berger, A., Hill, M., and Zeidman, B., "Software and RTOS synthesis: The next step in software development?" *Programmable Logic Design Line* (www.pldesignline.com), February 27, 2008.
24. Zeidman, R., "Iterative Filtering of Retrieved Information to Increase Relevance," *Journal of Systemics, Cybernetics and Informatics*, Vol. 5 No. 6, 2007, pp 91-96 (reprint).

25. Hoehn, T. and Zeidman, B., "Grid-Enabling Resource Intensive Applications," *Dr. Dobb's Journal*, November 2007, pp 22-28.
26. Zeidman, R., "Iterative Filtering of Retrieved Information to Increase Relevance," The 11th World Multi-Conference on Systemics, Cybernetics and Informatics, July 11, 2007.
27. Zeidman, B., "Who Stole My Software?" High Technology Crime Investigation Association, May 10, 2007.
28. Zeidman, B., "How to choose an RTOS for your FPGA and ASIC designs," *Programmable Logic Design Line* (www.pldesignline.com), May 10, 2007.
29. Zeidman, B., "Sharing Videos With Invisible Commercials," The Thirty Third Asilomar Microcomputer Workshop, April 18, 2007.
30. Zeidman, B., "Real Time Operating Systems for Systems on a Chip," IEEE Consultants Network of Silicon Valley, April 17, 2007.
31. Zeidman, B., "What, Exactly, Is Software Plagiarism?" *Intellectual Property Today*, February 2007.
32. Zeidman, B., "Software Synthesis for Embedded Systems," Silicon Valley Code Camp, October 8, 2006.
33. Zeidman, B., "Who Stole My Code," Silicon Valley Code Camp, October 8, 2006.
34. Zeidman, B., "Software Source Code Correlation," 5th IEEE/ACIS International Conference on Computer and Information Science, July 12, 2006.
35. Zeidman, B., "The Death of the Structured ASIC," *Chip Design*, April/May, 2006 (reprint).
36. Zeidman, B., "The Death of the Structured ASIC," *Programmable Logic Design Line* (www.pldesignline.com), April 18, 2006.
37. Zeidman, B., "All about FPGAs," *Programmable Logic Design Line* (www.pldesignline.com), March 22, 2006.
38. Zeidman, B., "The pluses and minuses of being a consultant," IEEE Gold, Santa Clara Valley Chapter, February 9, 2006.
39. Zeidman, B., "Using software synthesis for multiprocessor OS and software development," *Embedded.com* (www.embedded.com), January 6, 2006.
40. Zeidman, B., "Implementing Integrated Hardware/Software Projects With High Level Languages and Tools," Boston Section of the IEEE, December 1, 2005.
41. Zeidman, B., "Back to the basics: Programmable Systems on a Chip," *Embedded.com* (www.embedded.com), July 27, 2005.
42. Zeidman, B., "Who Stole My Code?" The Thirty First Asilomar Microcomputer Workshop, April 21, 2005.
43. Zeidman, B., "Software Synthesis for OS-Independent Coding," *Dr. Dobb's Journal*, April 2005, pp 58-63.
44. Zeidman, B., "RTOS Synthesis for Embedded Systems," Server Blade Summit, March 24, 2005.
45. Zeidman, B., "RTOS Synthesis to Reduce Power Consumption," DesignCon 2005, February 1, 2005.
46. Zeidman, B., "Software synthesis for embedded systems," *Embedded Systems Programming*, February 2005, pp 36-43.
47. Zeidman, B., "Testing a Network Device Prototype in a Live Network," *Annual Review of Communications* Volume 4, 2004, p.803 (reprint).
48. Zeidman, B., "Roll Your Own Real-Time OS," *BladeLetter*, Q3 2004, p. 8.

49. Zeidman, B., "Detecting Source-Code Plagiarism," *Developer 2.0*, September 2004 (reprint).
50. Zeidman, B., "Detecting Source-Code Plagiarism," *Dr. Dobbs's Journal*, July 2004, pp 55-60.
51. Zeidman, B., "Software Synthesis for Embedded Systems," The Thirtieth Asilomar Microcomputer Workshop, April 27, 2004.
52. Zeidman, B., "Smart Pills: The Royal Treatment," *IP Law & Business*, March 2004.
53. Zeidman, B., "Emulating/Prototyping a Network Device in a Live Network," DesignCon 2004, February 3, 2004.
54. Zeidman, B., "Software synthesis is productive for system design," *EE Times* (online), January 12, 2004.
55. Zeidman, B., "Guidelines for Effective E-Learning," *Chief Learning Officer*, December, 2003, pp. 24-31.
56. Zeidman, B., "Universal Design Methodology," *Embedded Systems Programming*, December, 2003, pp 55-56.
57. Zeidman, B., "Testing a network device in a live network," *EE Times*, October 20, 2003, p.92 (reprint).
58. Zeidman, B., "Test Your Next Hardware Design—in a Live Network," *Communication Systems Design*, October 2003, p.18.
59. Zeidman, B., "Platform FPGAs to Prevail," *Embedded Systems Programming*, November, 2003, pp.28-31.
60. Zeidman, B., "The Universal Design Methodology -- taking hardware from conception through production," *EDN*, December 26, 2002, p53.
61. Zeidman, B., "FPGAs vs. ASICs for Networking," Network Processor Conference West, October 2002, 11pp.
62. Zeidman, B., "How to Start A Consulting Business," *Embedded Systems Programming*, December 2000, pp161-163.
63. Zeidman, B., "An Introduction to Remote Backup," *Disaster Recovery Journal*, Vol. 9 No. 2, April/May/June 1996, p48.
64. Zeidman, B., "Testing System Memory Quickly and Efficiently," Design SuperCon 96 conference, February 1, 1996, 14pp.
65. Zeidman, B., "Remote Backup - Transmitting Critical Data Over Phone Lines for Offsite Storage," JAMCON '95 Communications Conference, August 20, 1995, pp97-99.
66. Zeidman, B., "Testing Memory Quickly," *Embedded Systems Programming*, Aug 1995, pp68-75
67. Zeidman, B., "Read-Ahead Logic: An Alternative to Cache," Design SuperCon 95 conference, March 1, 1995, 6pp.
68. Zeidman, B., "How to Make Money as a Consultant," *Income Opportunities*, Dec 1993, pp48-62
69. Zeidman, B., "Interleaving DRAMs for Faster Access," *ASIC & EDA*, November 1993, pp24-34.
70. Zeidman, B., "Starting Up Your Engineering Consulting Business," *High Technology Careers*, April/May 1993, p24.
71. Zeidman, B., "New Film Software for Independent Producers," *CUE*, September 1992, pp6-7.
72. Hafeman, D. and Zeidman, B., "Memory Architectures Compound RISC's Gains," *Electronic Design*, July 11, 1991, pp71-82.

73. Flynn, M. J., Zeidman, R. and Lochner, E., "Sparse Distributed Memory Prototype: Address Module Hardware Guide," Stanford University Computer Systems Laboratory CSL-TR-88-373, December 1988 72pp.
74. Flynn, M. J., Kanerva, P., Ahanin, B., Bhadkamkar, N., Flaherty, P., Hickey, P., Lochner, E., Webber, K., and Zeidman, R., "Sparse Distributed Memory Prototype: Principles of Operation," Stanford University Computer Systems Laboratory CSL-TR-88-338, December 1988 96pp.

PATENTS

I have written and filed the following patents with the US Patent and Trademark Office:

1. Zeidman, Robert M., "Visual tool for developing real time task management code," U.S. Patent 6,934,947.
2. Zeidman, Robert M., "Method for connecting a hardware emulator to a network," U.S. Patent 7,050,962.
3. Zeidman, Robert M., Hafeman, Daniel R., Barr, Michael, "Method and apparatus for synthesizing a hardware system from a software description," U.S. Patent 7,210,116.
4. Zeidman, Robert M., "Apparatus and method for connecting hardware to a circuit simulation," U.S. Patent 7,266,490, RE 42,227.
5. Zeidman, Robert M., "Software tool for detecting plagiarism in computer source code," U.S. Patent 7,503,035.
6. Zeidman, Robert M., Hafeman, Daniel R., Barr, Michael, "Method and apparatus for synthesizing a hardware system from a software description," U.S. Patent 7,620,928.
7. Zeidman, Robert M., Hafeman, Daniel R., "Method and apparatus for emulating a hardware/software system using a computer," U.S. Patent 7,647,583.
8. Zeidman, Robert M., "Detecting plagiarism in computer source code," U.S. Patent 7,823,127.
9. Zeidman, Robert M., "Apparatus and method for connecting hardware to a circuit simulation," U.S. Patent 7,835,897.
10. Zeidman, Robert M., "Software tool for synthesizing a real-time operating system," U.S. Patent 7,882,488.
11. Zeidman, Robert M. and Snider, Gregory, "Using readily available driver and application source code with a synthesized operating system," U.S. Patent 7,900,187.
12. Zeidman, Robert M., Hafeman, Daniel R., Barr, Michael, "Method and apparatus for synthesizing a hardware system from a software description," U.S. Patent 7,945,879.
13. Zeidman, Robert M., "System and method for connecting a logic circuit simulation to a network," U.S. Patent 8,160,863.
14. Zeidman, Robert M. "Use of hardware peripheral devices with software simulations," USPTO application number 12/946,721 (allowed).
15. Zeidman, Robert M., "Detecting copied computer source code by examining computer object code," USPTO application number 12/214,128 (allowed).
16. Zeidman, Robert M., "Method for advertisers to sponsor broadcasts without commercials," USPTO application number 09/767,819 (pending).
17. Zeidman, Robert, "Software for filtering the results of a software source code comparison" USPTO application number 11/467,155 (pending).
18. Zeidman, Robert M., "Software tool for detecting plagiarism in computer source code," USPTO application number 12/217,711 (pending).

19. Zeidman, Robert, and Hoehn, Timothy, "Searching the internet for common elements in a document in order to detect plagiarism," USPTO application number 12/253,249 (pending).
20. Mylroie, Steve and Zeidman, Robert M., "Detecting plagiarism in computer markup language files," USPTO application number 12/870,817 (pending).
21. Zeidman, Robert M., "Detecting plagiarism in computer source code," USPTO application number 12/871,808 (pending).

TRAINING/TEACHING EXPERIENCE

I have presented seminars and courses on the following topics:

- Analysis of Software Copyright Infringement Cases
- ASIC Design
- CPLD Design
- Detecting Software IP Theft
- Electrical Engineering for non-EEs
- Finding and Utilizing Technical Consultants for IP Litigation
- FPGA Design
- FPGAs vs. ASICs for Networking
- The History of Digital Hardware Design
- How to Start a Consulting Business
- Investigating Technology Theft
- Measuring Software Changes with the CLOC Method
- Memory Architectures
- Patents
- Programmable Systems on a Chip (SOCs)
- Real-Time Operating Systems for SOCs
- Software Intellectual Property
- Software Synthesis
- Starting a Consulting Business
- Technical Consultants for IP Litigation
- Testing Memory
- Universal Design Methodology
- Verilog and HDLs

At the following places:

- Cogswell College
- Cornell Entrepreneur Network
- DesignCon
- Eastcon
- Embedded Systems Conference Boston
- Embedded Systems Conference Chicago
- Embedded Systems Conference Europe
- Embedded Systems Conference India
- Embedded Systems Conference Silicon Valley
- Gigabit Ethernet Conference
- High-Level Electronic System Design Conference
- High Technology Crime Investigation Association
- Institute of Electrical and Electronics Engineers
- Network Processors Conference West
- Northcon
- Palo Alto Area Bar Association
- PCB Design East

- PCB Design West
- Semizone.com
- Southcon
- Stanford University
- Westcon
- World Intellectual Property Technical Forum